



Jeroen Hol

**Projektdirektiv**

2006-01-19

Sida 1

<b>Projektnamn</b>	<b>Micro EFIS</b>
<b>Beställare</b>	Jeroen Hol (ISY – Reglerteknik)
<b>Projektledare</b>	Student
<b>Projektbeslut</b>	Jan-Erik Strömberg (DST Control) Jeroen Hol (ISY – Reglerteknik)
<b>Projekttid</b>	Spring 2005, final presentation at 060519
<b>Rapportering</b>	Specification of requirements System outline Project plan including list of activities Time plan Test plan Test results Meeting minutes including status report Technical documentation Reflection Oral presentation Website
<b>Parter</b>	Kund: Jan-Erik Strömberg (DST Control) Beställare: Jeroen Hol (ISY – Reglerteknik) Examinator: Anders Hansson (ISY Reglerteknik) Projektgrupp: 4-6 students

<b>Projekts syfte</b>	The projects aim is to design and implement a functional prototype of a Micro EFIS (Electronic Flight Information System).
<b>Projekts mål och effekt</b>	The project shall generate the following concrete results <ul style="list-style-type: none"><li>• A functional prototype</li><li>• Validation of the prototype</li></ul>
<b>Bakgrund – samt referenser till andra projekt eller dokument</b>	The purpose of the Micro EFIS is to increase the safety of manned flight by providing an affordable backup flight instrument for the General Aviation market. The unit is intended as a backup system for a number of existing mechanical instruments. <ul style="list-style-type: none"><li>• Requirements specifications for Micro EFIS – Function and</li></ul>

**Dokumenthistorik**

<b>Version nr</b>	<b>Datum</b>	<b>Beskrivning</b>	<b>Sign</b>
<b>Version 0.1</b>	2005-12-07	First draft	JH
<b>Version 0.2</b>	2005-12-21	Incorporation of DST demands	JH
<b>Version 1.0</b>	2006-01-19	Final version	JH

Projektkursen namn: TSRT71

Dokumentansvarig: Jeroen Hol  
ProjektdirektivMicroEFIS

LIPs

ChrKr

CKr



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## Projektdirektiv

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Sida 2

	Interfaces
Delleveranser	<ul style="list-style-type: none"><li>• Status report and time reports per item and per person once a week to the beställare.</li><li>• At BP2: System outline, project plan and time plan.</li><li>• At BP5: Delivery of functional prototype</li><li>• At BP6: Technical documentation, evaluation</li><li>• Documentation including<ul style="list-style-type: none"><li>- Documentation of the modularisation of the firmware (which modules are defined and how do they interact, i.e. module contracts)</li><li>- Documentation of the module functions and implementations</li><li>- Documentation of software if any used to implement the prototype (in case a soft kernel is used)</li><li>- Documentation of software used on a host for upgrade of the target firmware.</li><li>- A "User's Guide" for the implemented prototype</li></ul></li></ul>

Projektdeltagare	Capabilities: Digital circuit theory, programming experience, English. Roles in the project: Projektledare, dokumentansvarig, testansvarig, kundansvarig, designansvarig, implementationansvarig.
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Kontakter	Beställare: Jeroen Hol (ISY - Reglerteknik) Handledare: Janne Harju (ISY - Reglerteknik) Employees of DST Control
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Införandebeslut	Taken by beställare at BP2.
Inköpsansvar	All required hard and software is provided by DST Control
Driftansvar	DST Control

Kostnader	20 hours of supervision by the handledare, supervision by DST
Finansiering/ Kostnadssätt	DST Control

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